

Position: One Professor Position

Smart power grid group, Power electronics field in School of Engineering

Department: Electrical and Electric Engineering, School of Engineering.

Research Group: Smart power grid group, Power electronics field in School of Engineering.
(Department-transverse research group in the School of Engineering)

Research Field: Smart power grid group, Power electronics field

Areas related to power electronics (power electronics, electric power systems, active use of renewable energy resources, battery energy storage systems, electric machinery, and control engineering).

Qualification Requirements:

1. Applicants are required to have a doctoral degree or Ph.D in Electrical and Electronic Engineering or a related field.
2. Applicants should have ability to teach undergraduate courses of electrical and electronic engineering and graduate courses of power electronics.
3. Applicants should have globally excellent research performance to lead the research field above.

Employment Date: As early as possible after April 1, 2018.

Employment Details: Permanent, monthly salary system, retirement age: 65.

http://www.somuka.titech.ac.jp/reiki_int/reiki_honbun/x385RG00000136.html (in Japanese)

http://www.somuka.titech.ac.jp/reiki_int/reiki_honbun/x385RG00000152.html (in Japanese)

Application Documents:

1. Curriculum vitae with a photograph, e-mail and postal addresses, including education records and professional career records.
2. Research achievements: a list of peer-reviewed papers, articles presented at international conferences, review papers, commentaries, books, patents and h-index.
3. Copies of five representative papers.
4. One page summary of academic significance of five representative papers above.
5. One page summary of past educational activities.

6. A list of awards related to the research achievements above.
7. A list of external or competitive funds for research or education including representative and share for each fund.
8. A list of society activities.
9. One page summary of the research plan.
10. One page summary of the education plan.
11. A list of four references with affiliations and e-mail addresses. Two of them should be resident and other two should be non-resident of Japan.

Application Deadline: Oct. 31, 2017. (Tuesday)

Mailing Address for the Application:

Prof. Koichi YASUOKA

Electrical and Electric Engineering, School of Engineering

Tokyo Institute of Technology

S3-3, 2-12-1 Ookayama, Meguro-ku, Tokyo 152-8552, Japan

Inquiries: yasuoka@ee.titech.ac.jp

Notice:

1. Applicants should send the PDF files of application documents stored in a CD-ROM or DVD-ROM disk by registered postal mail or courier service. On the envelope, write "Application Documents for the Professorship" in red. Paper documents are not required. We do not accept e-mail application. The disk submitted shall not be returned.
2. Short technical seminar or trial lesson might be requested for the selection.
3. Tokyo Institute of Technology provides start-up funding through an internal support system, and selected applicants have chance to use this system.
4. Tokyo Institute of Technology will ensure equal opportunities for participation of women and foreigners.